LISTING OF THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended): A system for signal conversion, comprising:

a spreading code generator that produces a direct sequence spread spectrum (DS-SS) signal and a frequency hopped spread spectrum (FH-SS) signal and combines the DS-SS and FH-SS signals to provide a spreading signal;

a spreader that combines a the spreading signal with an input signal to provide a spread input signal;

a signal converter that converts the spread input signal from a first domain to a second domain to provide a converted spread input signal; and

a despreader that despreads the converted spread input signal to provide the input signal in the second domain.

2-4. (Cancelled).

5. (Original) The system of claim 1, further comprising a feedback loop coupling the despreader to the spreader for time aligning the despreading with the spreading.

6. (Original) The system of claim 1, wherein the first domain is one of a digital domain and an analog domain and the second domain is the other of the digital domain and the analog domain.

7. (Original) The system of claim 1, further comprising a mixer for frequency converting the spread input signal prior to despreading.

8. (Original) The system of claim 1, wherein the signal converter is one of a delta-sigma analog-to-digital converter (ADC) and a delta-sigma digital-to-analog converter (DAC).

9. (Original) The system of claim 1, further comprising a clipping component that reduces peaks associated with the spread input signal, the despreader mitigates degradation and out-of-band (OOB) emissions associated with the peak reduction.

10. (Original) The system of claim 1, wherein at least one of the spreader and the despreader circuit comprises a mixer.

11. (Original) A receiver comprising the system of claim 1.

12. (Original) A transmitter comprising the system of claim 1.

13. (Original) A signal conversion system comprising:

a spreading code generator that produces a direct sequence spread spectrum (DS-SS) signal;

a spreading circuit that receives an input signal and combines the input signal with the DS-SS signal to provide a spread input signal;

a clipping component that reduces peaks associated with the spread input signal; and

a despreading circuit that despreads the peak reduced spread input signal.

14. (Original) The system of claim 13, wherein at least one of the spreading circuit and despreading circuit comprises a mixer.

15. (Original) The system of claim 13, further comprising a signal converter that converts the spread input signal from a first domain to second domain, the signal converter being one of a digital-to-analog converter (DAC) and an analog-to-digital converter (ADC).

16. (Original) The system of claim 15, the signal converter being one of a delta-sigma DAC and a delta-sigma ADC.

17. (Original) The system of claim 15, further comprising a second signal converter for converting the spread signal from the second domain to the first domain.

18. (Original) The system of claim 15, further comprising a mixer for frequency converting the spread input signal one of before signal conversion and after signal conversion.

19. (Currently Amended): A method for signal conversion, comprising:

combining a direct sequence spread spectrum (DS-SS) signal with a frequency hopped spread spectrum (FH-SS) signal to provide a spreading signal;

spreading a signal with a ~~direct sequence spread spectrum (DS-SS)~~ the spreading signal in a first domain;

converting the spread signal from the first domain to a second domain; and

despreading the signal ~~with a DS-SS signal~~ in the second domain.

20. (Cancelled).

21. (Original) The method of claim 19, wherein the first domain is one of a digital domain and an analog domain and the second domain is the other of the digital domain and the analog domain.

22. (Original) The method of claim 19, further comprising frequency converting the signal to an intermediate frequency.

23. (Original): The method of claim 19, further comprising:

receiving the signal from an antenna;

filtering the signal;

amplifying the signal; and

converting the signal to an intermediate frequency signal prior to spreading the signal.

24. (Original) The method of claim 19, further comprising:

converting the signal to a radio transmission frequency;

filtering the signal;

amplifying the signal; and

transmitting the signal over an antenna

25. (Original) The method of claim 19, further comprising clipping the signal to reduce peaks associated with the signal.

26. (Currently Amended): A communication device comprising:

means for generating a direct sequence spread spectrum (DS-SS) signal;

means for combining the DS-SS signal with an input signal to produce a spread input signal;

means for clipping the spread input signal to remove peaks:

means for converting the spread input signal from a first domain to a second domain; and

means for dispreading the spread input signal in the second domain.

27. (Cancelled).